
Three cell flying capacitor inverter for dielectric barrier discharge plasma applications


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I. INTRODUCTION

Dielectric barrier discharges (DBD) also known as silent discharges, are commonly produced in parallel plates or coaxial cells at near atmospheric pressure (1-3 atm). Plasmas in a dielectric barrier discharge cell (DBDC) are generated by applying high voltage (HV) to a pair of electrodes, separated by a gap, where at least one of these is covered by a dielectric sheet, usually made of quartz, glass or ceramic. These discharges are frequently used in industrial ozone generation [1] or in the treatment of air pollutants [2], in particular, solvents or volatile organic compounds (VOC) which are mainly produced around chemical plants [3]. The conditions required to produce non-equilibrium plasmas are easier to attain with DBDC than by alternative methods such as low pressure discharges, high pressure discharges with fast pulses, or electron beam injection. DBD are generated in a high pressure gas and consist of a multitude of short lived current filaments [4]. Due to the short duration of these micro discharge channels and the prevailing low ion mobility, the kinetic energy in silent discharges is principally transferred to electrons, which become hot, unlike the other species. This behavior provides a significant electronic excitation of molecules. As a consequence, the plasma promotes efficient thermochemical reactions processes, which are desirable in the destruction of hazardous organic compounds [5]. In order to achieve a stable DBD performance, the incumbent power sources usually provide HV AC, from some tens of Hz [6] up to the order of several kHz [7] with amplitudes from 1 to 100 kV. Thus, several authors have resorted to resonant inverters either in a half-bridge configuration [8] or in a full-bridge one, operating at tens of kHz [9]. Nevertheless, these converters, however efficient, are constrained to operate in a narrow frequency range.

The present report presents some early results of a complex system which is mainly constituted by a DBDC made of parallel plates biased by a coupled transformer whose primary is driven by a voltage source inverter (VSI) configured on the basis of a half-bridge three cell flying capacitor inverter (TCFCI). Pulse width modulation by means of phase shifting (PS-PWM) has been applied in order to drive the TCFCI. The latter has been implemented through a hybrid electronic circuit, which generates six commutation pulses for the high power switches that constitute the TCFCI. An appropriate operating system provides a four-level chopped voltage at the output inverter. The correct performance of the control strategy enables a good open-loop inverter behavior due to a natural balance originated in the inherent current harmonics. An electrical characterization of the system, including efficiency calculations, has been included.

II. SYSTEM DESCRIPTION

The electric circuit representation of the proposed DBD system is shown in Fig. 1. It is mainly composed by a variable autotransformer supplying an adjustable AC voltage level within 0-220 V to a non controlled full wave rectifying bridge. It delivers in turn a DC voltage to a voltage divider constituted by two capacitors, $C_{3S}$ and $C_{3G}$, along with a $C_{SF}$ one, functioning like input filter. Thus, a bipolar DC voltage is applied to the input of a TCFCI which is assembled by means of three commutation cells. Each one of the last is implemented through a hybrid electronic circuit, which generates six commutation pulses for the high power switches that constitute the TCFCI. An appropriate operating system provides a four-level chopped voltage at the output inverter. The correct performance of the control strategy enables a good open-loop inverter behavior due to a natural balance originated in the inherent current harmonics. An electrical characterization of the system, including efficiency calculations, has been included.
defined by $V_{C_1} = \frac{1}{2}V$ and $V_{C_2} = \frac{3}{2}V$ respectively. Meanwhile, the capacitance value can be determined as:

$$C_k = \frac{I_{\text{max}}}{f_{\text{SW}}\Delta V_{C_k}} \tag{1}$$

where: $\Delta V_{C_k}$ is the maximum permissible ripple voltage provided by the floating capacitor, $I_{\text{max}}$ is the maximum DC current inverter and, $f_{\text{SW}}$ is the switching frequency. A converter operation guaranteeing open loop stability is determined by two main factors: (i) an adequate sequence of the commutation pulses imposed to the power switches and (ii) the correct effective voltage levels $V_{C_k}$ among flying capacitors.

A PS-PWM strategy was chosen in order to modulate the pulses width applied to VSI power switches as seen in Fig. 2. The system is supported by a sinusoidal waveform generator which provides a reference $f_{\text{SW}} = 200$ kHz signal to an operational amplifiers stage, where three $\frac{\pi}{2}$ out of phase sinusoidal signals are generated. The latter are applied to a logarithmic amplifier stage, which in turn produces triangular waveforms meant to act as carrier signals to be compared to the modulating one (produced by a digital frequency divider, and later on integrated, so to obtain a sinusoidal waveform with frequency $f_m = \frac{1}{20}f_{\text{SW}} = 12.5$ kHz). The modulation process is developed by comparing the modulating sinusoidal signal and the triangular waveforms, resulting in three different sequences of pulses which are conditioned in order to meet TTL levels. After that, the resulting pulses are optically isolated between the command circuit and the power switches by means of optic fiber link connections reducing the electromagnetic interference (EMI). Likewise, another circuit has been implemented with a magnetic coupling provided that the firing signal of each power switch should be applied with respect to a floating voltage. Thus, driver outputs are coupled by means of pulse transformers and commutation signals, given that the switches need additional components, as it is shown in the firing circuit detail of Fig. 2. There, capacitor $C_{C_1}$ provides the restoration voltage for the inductance of the primary winding transformer; both of these components integrating an LC circuit. Thus a resistor $R_c$ is added with the purpose of preventing any resonant effect. Additionally, capacitor $C_{C_2}$ along with diode $D_c$ are used to re-establish the driver’s original amplitude. Finally, the circuit containing capacitor $C$, resistor $R$ and diode $D$ modifies the dynamical behavior of the pulse signal provided from the secondary winding of the pulse transformer.

An implemented Simulink/Matlab® model of the proposed system makes possible to obtain a valuable approaching of its dynamic performance. As the simulation outcome in Fig. 3.a depicts, three triangular mutually $\frac{\pi}{2}$ out of phase signals are compared independently from the modulating sinusoidal signal at the frequency $f_m$. When the modulating amplitude exceeds that of the carrying signal, a voltage pulse is generated in order to start the power switch and therefore increase the power supply. The resulting pulse sequence is presented in Fig. 3.b. On its part, Fig. 4.a displays the voltage output of the TCFCI in vacuum in order to demonstrate that it is actually modulated by three different voltage levels imposed by the flying capacitors. In addition, the modulated voltage applied to the primary winding generates a quasi-sinusoidal current waveform. Thus, the electric energy transferred to the secondary winding increases the input voltage so as to get a well established discharge. As it can be observed in Fig. 4.b, the discharge current and voltage waveform resulting from the simulation closely resemble the archetypical discharges obtained experimentally as in [11].
FIG. 3: Simulation outcome from the implemented modulation strategy circuit: (a) the three carrier signals are compared with respect to the modulating signal; (b) resulting sequences to be applied as control signals to the power switches.

III. EXPERIMENTAL SET-UP

The instrumental arrangement used in this study is represented in Fig. 5, where several photos of previously described TCFCI circuits can be identified. A top down view of the implemented DBDC is depicted in Fig. 6.a; meanwhile, Fig. 6.b illustrates the inner cell structure. The DBDC is constituted by two electrodes of 0.12 m diameter made of aluminum separated by two glass dielectric sheets, 0.002 m thick, and a gap $l = 0.002$ m between them. Thus, the total cell capacitance of the DBDC is given by an equivalent capacitance $C_d = \varepsilon_0 \varepsilon_r S/2l$, where the industrial glass dielectric relative permittivity is $\varepsilon_r = 4.9$, $\varepsilon_0 = 8.8541 \times 10^{-12}$ F/m is the permittivity in vacuum, and $S = 0.0113$ m$^2$ is the total calculated electrode surface. Hence, the DBDC calculated capacitance turns out to be $C_d = 122$ pF. The gas capacitance $C_g$ was calculated for argon and helium, in both cases its magnitude being near 50 pF. The DBDC gas supply was endowed with a pipeline connected from two gas containers through a gas mixer and mass flow meters. In all cases, electric discharges were generated at atmospheric pressure with gas mass flow of 2.0 LPM.

FIG. 4: Simulation outcome of the primary winding voltage supply and resulting current intensity. (b) Applied cell voltage and its resulting current in the case of an argon discharge.

FIG. 5: Block scheme of the experimental layout.
The TCFCI output voltage and current \((v_p(t), i_p(t))\) and DBDC applied voltage and discharge current \((v_s(t), i_s(t))\) are monitored by means of a (Tektronix TDS2014) oscilloscope equipped with a HV probe (Tektronix P6015) and a current sensor (Stangenes model 0.5-1.0 W). We consider as an illustration the DBDC applied voltage and its resulting current waveform \((v_p(t), i_p(t))\) captured during the stable phase of the discharge, shown in Fig. 7. From these waveforms, the peak values of \(v_p\) about 28 V and of \(i_p\) about 5.8 A have been determined. Thus, real power applied to the primary winding of the transformer is given by:

\[
P_p(t) = v_{p\,rms}(t) \cdot i_{p\,rms}(t) \cdot \cos(\theta)
\]

which, considering that measured value of \(\theta = 85^0\), amounts to \(P_p(t) \approx 7.1\) W; an expected result as the DBDC exhibits a predominantly capacitive conduct.

Fig. 8 portrays the voltage and current wave forms measured at the DBDC plugs. The \(v_s(t)\) peak value reaches about 1.75 kV while that of \(i_s(t)\) is 5 mA, estimated from its first harmonic. Although the instantaneous power absorbed by the DBDC can be directly assessed from the product of \(v_s(t)\) and \(i_s(t)\), it can also be assessed from the charge stored in the cell, \(Q_s(t)\) (here represented by the measured voltage \(v_m(t)\) of a reference capacitance \(C_m\), keeping in mind that \(Q_s(t) = C_m v_m(t)\) versus the applied voltage, \(v_s(t)\) (Fig. 9) [12],[13]. In this work we considered that \(C_m \sim 2 \mu F\) is the reference capacitance shown in Fig. 5. \(V_m\) is the voltage amplitude per division on the vertical axis of Fig. 9, namely 20 mV/div, and \(V_p 1\) kV/div is the magnitude of a division on the horizontal axis. Thus, the computed energy stored by the cell \(E = 0.196\) mJ while the power supplied to the cell \(P_c = fE\), where \(f\) is the frequency of the excitation signal applied to the DBDC, namely, \(P_c = 2.45\) W. In other words, the power transferred from the transformer secondary to the cell attains an electric efficiency of 34.5% in the case of argon discharges.

The \(v_p(t)\) peak level, which is about 12.5 V, and the corresponding \(i_p(t)\) peak, around 3 A, have been obtained from the characteristic signals applied to the transformer primary as seen in Fig. 10 in the case of a helium discharge. Thus, the real power applied to the primary winding, considering that \(\theta = 85^0\) leads to a value \(P_p(t) \approx 2.61\) W. The signal in the DBDC is depicted in Fig. 11. The peak value of \(v_s(t)\) is around 1 kV, and, approaching \(i_s(t)\) by its first harmonic, its maximal value reaches 2 mA.

Therefore, as \(v_m(t) = Q_s(t)/C_m\) then it is possible to get the charge stocked \(Q_s(t)\) in the reactor. Thus, in the case of helium discharges from the \(v_m(t)\) versus \(v_s(t)\) characteristic as
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**FIG. 9:** $v_m(t)$ versus $v_s(t)$ Lissajous waveform in the DBDC with a 12.5 kHz excitation frequency when fed with argon.

**FIG. 10:** Experimental time patterns of the applied voltage $v_p(t)$ and its associated current $i_p(t)$, in a 2 LPM flow of helium at room pressure

**FIG. 11:** $v_s(t)$ and $i_s(t)$ time patterns measured in the DBDC, working on a 2 LPM helium flow at room pressure

**FIG. 12:** $v_m(t)$ versus $v_s(t)$ waveform of a DBDC with helium and a 12.5 kHz excitation frequency

A second method for calculating the power consumed by the DBDC yields the following Manley expression [13]:

$$P_c = 4V_bC_d f \left[ V_M - V_b \left( \frac{C_d + C_g}{C_d} \right) \right]$$

(3)

where: $V_M$ is the maximal amount of voltage supplied to the cell. Consequentially, operating it on argon requires 2.64 W which implies an 34.6% efficiency while, in helium, the required power, 0.564 W, improves the efficiency up to 21.6%.

**FIG. 13:** Waveforms of the instantaneous power $v_s(t) \cdot i_s(t)$ applied to the DBDC with a 2 l/min argon flow at room pressure

A final analysis of the instantaneous power of the signals applied to the DBDC, that is, voltage and current intensity at the secondary of the step up transformer, enables one to obtain the actual value of the applied power in both gases, as the one shown in Fig. 12, then $V_m = 10$ MV/div, $V_s = 1$ kV/div, whereby $E_c = 20 \mu$J/div$^2$ and, consequentially, $E = 0.045$ mJ. Finally, $P_c = 0.562$ W so that the power transferred from the primary to the cell can be estimated as 21.53%.
suggested by Fig. 13 (argon) and Fig. 14 (helium). In a similar fashion, Table I gathers the results of the power estimation by all three methods including the respective efficiency values with both gases.

It is obvious that the first and third methods seem to agree better, which could be expected from their being obtained from experimental values. In contrast, the second one depends on the breakdown voltage, rather roughly estimated from the experimental conditions.

IV. CONCLUSIONS

The design TCFCI built out of high power high frequency semiconductors intended to generate room pressure dielectric barrier discharges has been described. Its hybrid system of command consists of a relatively simple and stable cycle which simplifies the handling of reference signals in order to accomplish the required power switch commutation through intermediate circuits able to isolate the low power stage from the high one. Thus, the use of an insulation stage reduces electromagnetic interference significantly along with the transients typical of the high power systems.

The open loop operation of the proposed system maintains a stable behaviour conducive to a well ordered voltage distribution in the floated capacitors given the selected modulation technique. Although the proposed design allows one to adjust the frequency of the reference sinusoidal signal so to modify the DBDC excitation frequency, the 12.5 kHz frequency was maintained throughout the study provided that this is the maximal possible value for the whole system. Thus, when a different modulation system is in place, e.g., a square pulse one, it is possible to raise the frequency even further with the consequent improvement in the general efficiency, albeit with some change of components.

The static converter developed for the DBDC discharge generation has been satisfactorily tested by establishing room pressure discharges in a parallel plate cell containing helium and argon, achieving efficiencies above 20 and 30 respectively. An innovative conversion characteristic of the proposed TCFCI is its high 12.5 kHz operation frequency, far beyond the 3 kHz one reported in similar inverters.

<table>
<thead>
<tr>
<th>Gas</th>
<th>Rhomboid method</th>
<th>Manley’s equation</th>
<th>Real power</th>
</tr>
</thead>
<tbody>
<tr>
<td>argon</td>
<td>2.45 W</td>
<td>3.64 W</td>
<td>2.34 W</td>
</tr>
<tr>
<td>helium</td>
<td>0.562 W</td>
<td>21.5%</td>
<td>0.575 W</td>
</tr>
</tbody>
</table>

TABLE I: Comparison of power values applied to the DBDC and their resulting efficiency according to three distinct calculation procedures

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